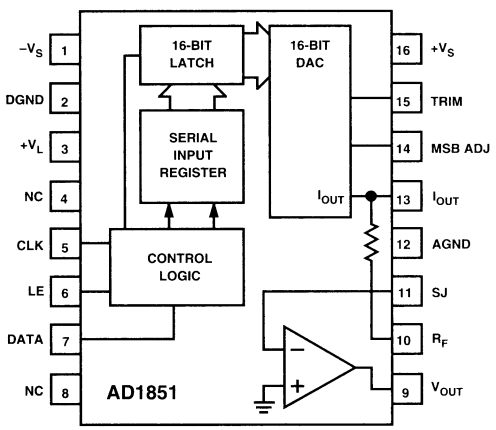
AD1851/AD1861–SPECIFICATIONS (T_A @ +25°C and ±5 V supplies, unless otherwise noted)

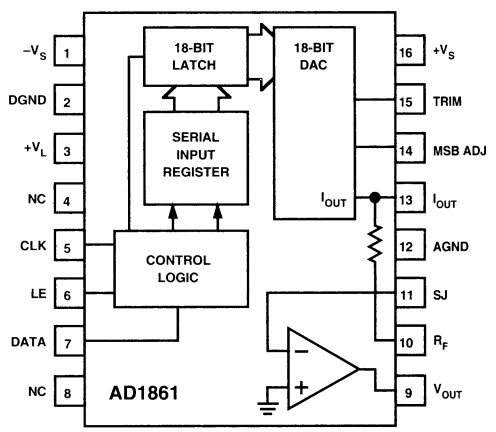
	Min	Typ	Max	Units
DIGITAL INPUTS				
V_{IH}	2.0		$+V_L$	V
V_{IL}			0.8	V
I_{IH}, $V_{IH} = V_L$			**1.0**	µA
I_{IL}, $V_{IL} = 0.4$			**–10**	µA
ACCURACY				
Gain Error		±1		%
Midscale Output Voltage		±10		mV
DRIFT (0°C to +70°C)				
Total Drift		±25		ppm of FSR/°C
Bipolar Zero Drift		±4		ppm of FSR/°C
SETTLING TIME (To ±0.0015% of FSR)				
Voltage Output				
6 V Step		1.5		µs
1 LSB Step		1.0		µs
Slew Rate		9		V/µs
Current Output				
1 mA Step 10 Ω to 100 Ω Load		350		ns
1 kΩ Load		350		ns
OUTPUT				
Voltage Output Configuration				
Bipolar Range	**±2.88**	±3.0	**±3.12**	V
Output Current	±8			mA
Output Impedance		0.1		Ω
Short Circuit Duration		Indefinite to Common		
Current Output Configuration				
Bipolar Range (±30%)		±1.0		mA
Output Impedance (±30%)		1.7		kΩ
POWER SUPPLY				
Voltage				
$+V_L$ and $+V_S$	4.75		5.25	V
$-V_S$	–5.25		–4.75	V
TEMPERATURE RANGE				
Specification	0	+25	+70	°C
Operation	–25		+70	°C
Storage	–60		+100	°C
WARM-UP TIME	1			min

Specifications subject to change without notice.



NC = NO CONNECT

AD1851 Functional Block Diagram



NC = NO CONNECT

AD1861 Functional Block Diagram